

Amendment to the Specification

Please replace the paragraph on Page 15, lines 2 - 11, with the following marked-up replacement paragraph:

-- The LM instruction requires three parameters. The first parameter is a register to be used as the starting location of the load operation, and the second is a register to be used as the ending location. The third parameter points to the beginning storage location of the value(s) to be loaded into the registers. Both the ~~CD~~ CS and CDS comparison instructions require three parameters. The first and third parameters are pointers to the values being compared. The second parameter is a pointer to a new value that will be copied in place of the value addressed by the first pointer if the first and third values are equal; otherwise, when the values are not equal, the value pointed to by the third pointer is copied into the location addressed by the first pointer. (Note that Compare and Swap is the technique mentioned in the previously-discussed IBM Technical Disclosure Bulletin article for performing atomic updates on AVL trees.) --